

[ means for fetching instructions for the central processing unit and for fetching instructions for the direct memory access processing unit on the bus." and substitute

--memory, and an instruction register. A method provides instructions from the instruction register to the central processing unit and comprises the steps of:

providing instruction groups to the instruction register from the memory wherein certain of the instruction groups include one or more operands or sequential instructions or both;

supplying, in succession from the instruction register, the one or more sequential instructions of the certain of the instruction groups to the central processing unit; and

selecting an operand from the one of the instruction groups for use by the central processing unit.--

Delete Page 3, line 33 through Page 7, line 31.

In the Abstract:

Lines 1-2 thereof, delete "A high performance microprocessor system using instruction groups is disclosed herein." and substitute /--A high-performance microprocessor system using instructions that access operands and instructions located relative to the current instruction group rather than located relative to the current instruction, as is the convention, is disclosed herein.--

In the Claims:

Rewrite claims 71-74, 77-78, 91-97 and 99-100 as follows:

71 (Twice Amended). A microprocessor system comprising:

a central processing unit;

memory;

a bus connecting said central processing unit to said memory;

instruction fetching means that are connected to said bus to fetch instruction groups via said bus from said memory, [each] certain of said instruction groups including at least one instruction that accesses operands or instructions or both, said operands and instructions being located relative to said instruction groups;

an instruction register for receiving a first of said instruction groups from said instruction fetching means, said first of said instruction groups including one or more operands or sequential instructions or both;